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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/876,160	06/08/2001	Masaharu Ikeda	20402/0625	6709	
7590 12/01/2004			EXAMINER		
Connolly Bove Lodge & Hutz LLP			CHAU, COREY P		
Suite 800 1990 M Street, N.W.			ART UNIT	PAPER NUMBER	
Washington, DC 20036-3425			2644		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati	ion No.	Applicant(s)			
Office Action Summary		09/876,1	09/876,160 IKEDA, MA		ARU		
		Examine	Examiner Art Unit				
	-	Corey P	Chau	2644			
Period fo	The MAILING DATE of this commur or Reply	nication appears on th	e cover sheet witl	h the correspondence a	ddress		
THE - Exte after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD F MAILING DATE OF THIS COMMUN nsions of time may be available under the provisions SIX (6) MONTHS from the mailing date of this com period for reply specified above is less than thirty (3) period for reply is specified above, the maximum so re to reply within the set or extended period for reply reply received by the Office later than three months ed patent term adjustment. See 37 CFR 1.704(b).	ICATION. s of 37 CFR 1.136(a). In no evenunication. 30) days, a reply within the statutory period will apply and vor will, by statute, cause the ap	vent, however, may a rep tutory minimum of thirty vill expire SIX (6) MONT plication to become ABA	ply be timely filed (30) days will be considered time HS from the mailing date of this NDONED (35 U.S.C. § 133).	ely. communication.		
Status							
1)⊠	Responsive to communication(s) file	ed on <u>7/13/04</u> .					
2a)⊠	This action is FINAL .	2b)☐ This action is	non-final.				
3)[3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
	closed in accordance with the pract	ice under Ex parte Q	uayle, 1935 C.D.	11, 453 O.G. 213.			
Disposit	ion of Claims						
5) <u>□</u> 6)⊠	Claim(s) 1-13 is/are pending in the 4a) Of the above claim(s) is/a Claim(s) is/are allowed. Claim(s) 1-13 is/are rejected. Claim(s) is/are objected to. Claim(s) are subject to restri	are withdrawn from co					
Applicat	ion Papers		•				
9)[The specification is objected to by the	ne Examiner.			•		
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.							
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11)	Replacement drawing sheet(s) including The oath or declaration is objected to	-		•			
Priority (under 35 U.S.C. § 119						
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
Attachmen	nt(s)						
1) Notice 2) Notice 3) Infor	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (mation Disclosure Statement(s) (PTO-1449 o er No(s)/Mail Date		Paper No(s)	ummary (PTO-413) /Mail Date formal Patent Application (PT	ГО-152)		

Art Unit: 2644

DETAILED ACTION

Response to Amendment

Claim Rejections - 35 USC § 112

- 1. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 2. Claims 1 and 12 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.
- 3. Claim 1 recites the limitations: "the circuit" in line 6; and "first amplification circuit" in lines 8, 9 and 11. There is insufficient antecedent basis for this limitation in the claim.
- 4. Claim 12 recites the limitation "The microphone according to claim 12" in line 1, which renders the claim indefinite because it is unclear as to what claim 12 depends on, however for examining purposes, claim 12 is treated as depending on claim 11.

Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claims 1, 2, and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 4491697 to Tanaka et al (hereinafter as Tanaka) in view of U.S. Patent No. 4443666 to Cote.

external electromagnetic interference.

Art Unit: 2644

7. Regarding Claim 1, Tanaka discloses a capacitor type of microphone (i.e. condenser microphone) having a microphone output terminal (Fig. 1), the microphone comprising movable electrode (i.e. one conductive vibrating plate); one fixed electrode arranged opposite to the vibrating plate (abstract; Fig. 1); a first amplification means (201,202); and second amplification means (206) cascaded to the first amplification means between an output terminal of the first amplification means and a microphone output terminal (abstract; Fig. 1). Tanaka does not expressly disclose a member for shielding the microphone from electromagnetic waves. However it would have been obvious to one having ordinary skill in the art to provide a member for shielding the microphone comprising the movable electrode, the fixed electrode, the first amplification means, and the second amplification means in order to avoid external electromagnetic interference as taught by Cote. Cote disclose a capacitor microphones in which a permanently polarized dielectric, or electret, is used requires a local preamplifier circuit because of their low output which must be well shielded to avoid external electromagnetic interference (column 1, lines 6-16; column 1, line 67 to column 2, line 5; column 2, lines 43-68). Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Tanaka with the teaching of Cote to shield the microphone comprising the movable electrode, the fixed electrode, the first amplification means, and the second amplification means in order to avoid

Page 3

8. Regarding Claim 8, Tanaka discloses a capacitor type of microphone (i.e. condenser microphone) having a microphone output terminal (Fig. 1), the microphone

Art Unit: 2644

comprising movable electrode (i.e. one conductive vibrating plate); one fixed electrode arranged opposite to the vibrating plate (abstract; Fig. 1); a first amplification circuit (201,202); and second amplification circuit (206) cascaded to the first amplification circuit between an output terminal of the first amplification circuit and a microphone output terminal (abstract: Fig. 1). Tanaka does not expressly disclose a member for shielding the microphone from electromagnetic waves. However it would have been obvious to one having ordinary skill in the art to provide a member for shielding the microphone comprising the movable electrode, the fixed electrode, the first amplification circuit, and the second amplification circuit in order to avoid external electromagnetic interference as taught by Cote. Cote disclose a capacitor microphones in which a permanently polarized dielectric, or electret, is used requires a local preamplifier circuit because of their low output which must be well shielded to avoid external electromagnetic interference (column 1, lines 6-16; column 1, line 67 to column 2, line 5; column 2, lines 43-68). Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Tanaka with the teaching of Cote to shield the microphone comprising the movable electrode, the fixed electrode, the first amplification circuit, and the second amplification circuit in order to avoid external electromagnetic interference.

9. Regarding Claim 2, Tanaka as modified discloses DC power supply connected to the drains of the first and second field effect transistor; first and second impedance elements connected between gates of field effect transistors and ground to hold the DC potential of each gate at ground level (i.e. second amplification circuit includes drive

Application/Control Number: 09/876,160 Page 5

Art Unit: 2644

means, in which a power supply to the drive means is configured so that the power is obtained as a constant current from outside the microphone via the microphone output terminal)(column 1, line 62 to column 2, line 10).

- 10. Claims 9-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 4491697 to Tanaka in view of U.S. Patent No. 4443666 to Cote as applied to claims 1, 2 and 8 above, and further in view of U.S. Patent No. 6104818 to Korner.
- 11. Regarding Claim 9, Tanaka as modified discloses a second amplification circuit (206), but it would have been obvious to one having ordinary skill in the art to utilize any equivalent second amplification circuit that would produce the same result. Korner for example, discloses a transistor that might also consist of a field effect transistor (Fig. 3; column 2, lines 30-46). It would have been obvious to one having ordinary skill in the art to employ any know equivalent second amplification circuit that would produce the same result, such as that of Korner. Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Tanaka with the teaching of Korner to utilize a FET as the second amplification circuit. Therefore the second amplification circuit has an FET structured into a gate-common amplification circuit (i.e. the gate is maintained at ac common), the FET having a source electrode receiving an output current of the first amplification circuit and current from a drain electrode of the FET passing to the microphone output terminal.

Art Unit: 2644

12. All elements of Claim 10 are comprehended by Claim 9. Claim 10 is rejected for the reasons stated above apropos to Claim 9.

- 13. Claim 11 is essentially similar to Claims 1 and 9 and is rejected for the reasons stated above apropos to Claims 1 and 9. The FET of Korner is structured into a gate-common, wherein the gate is maintained at ac common, therefore the gate is grounded.
- 14. Claim 12 is essentially similar to Claim 2 as is rejected for the reasons stated above apropos to Claim 2.
- 15. Claim 13 is essentially similar to Claim 9 and is rejected for the reasons stated apropos to Claim 9.
- 16. Claims 1, 3, 4, 5, 6, 7, 8, and 11 are rejected under 35 U.S.C. 103(a) as being unpatentable over U.S. Patent No. 6104818 to Korner in view of U.S. Patent No. 4443666 to Cote.
- 17. Regarding Claim 1, Korner discloses a capacitor type of microphone having a microphone output terminal, the microphone comprising: a movable electrode (22); a fixed electrode (24) arranged face to face to the movable electrode; first amplification means (20); and second amplification means (18) cascaded to the first amplification means between an output terminal of the first amplification means and a microphone output terminal (Fig. 3; column 3, lines 6-15). Korner does not expressly disclose a member for shielding the microphone from electromagnetic waves. However it would have been obvious to one having ordinary skill in the art to provide a member for shielding the microphone comprising the movable electrode, the fixed electrode, the first

Art Unit: 2644

amplification means, and the second amplification means in order to avoid external electromagnetic interference as taught by Cote. Cote disclose a capacitor microphones in which a permanently polarized dielectric, or electret, is used requires a local preamplifier circuit because of their low output which must be well shielded to avoid external electromagnetic interference (column 1, lines 6-16; column 1, line 67 to column 2, line 5; column 2, lines 43-68). Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Korner with the teaching of Cote to shield the microphone comprising the movable electrode, the fixed electrode, the first amplification means, and the second amplification means in order to avoid external electromagnetic interference.

18. Regarding Claim 8, Korner discloses a capacitor type of microphone having a microphone output terminal, the microphone comprising: a movable electrode (22); a fixed electrode (24) arranged face to face to the movable electrode; first amplification circuit (20); and second amplification circuit (18) cascaded to the first amplification circuit between an output terminal of the first amplification circuit and a microphone output terminal (Fig. 3; column 3, lines 6-15). Korner does not expressly disclose a member for shielding the microphone from electromagnetic waves. However it would have been obvious to one having ordinary skill in the art to provide a member for shielding the microphone comprising the movable electrode, the fixed electrode, the first amplification circuit, and the second amplification circuit in order to avoid external electromagnetic interference as taught by Cote. Cote disclose a capacitor microphones in which a permanently polarized dielectric, or electret, is used requires a local

Art Unit: 2644

preamplifier circuit because of their low output which must be well shielded to avoid external electromagnetic interference (column 1, lines 6-16; column 1, line 67 to column 2, line 5; column 2, lines 43-68). Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Korner with the teaching of Cote to shield the microphone comprising the movable electrode, the fixed electrode, the first amplification circuit, and the second amplification circuit in order to avoid external electromagnetic interference.

- 19. Regarding Claim 3, Korner as modified discloses a capacitor connected to a resistor wherein a resistance is for supplying a driver voltage to the microphone circuit (i.e. power supply to the drive means is configured so that the power is temporarily obtained for storage through the microphone output terminal according to voltage values and the stored voltage is used when obtaining the power is stopped) (abstract; Fig. 3, references 12 and 14).
- 20. Regarding Claim 4, Korner as modified discloses second amplification circuit (18) might also consist of a field effect transistor (column 2, lines 35-36). Therefore, the second amplification circuit is structured into a gate-common amplification circuit (i.e. the gate is maintained at ac common) (Figs. 3-6), wherein the FET has a source electrode of the FET receiving an output current of the first amplification circuit and current from a drain electrode of the FET passing the microphone output terminal (Fig. 3, references 18 and 20).
- 21. Regarding Claim 5, Korner as modified discloses second amplification circuit (18) has a junction type of transistor (Fig. 3). Therefore, the second amplification circuit is

Art Unit: 2644

structured into a base-common amplification circuit, wherein the transistor has an emitter receiving an output current of the first amplification circuit and current from a collector of the transistor passing the microphone output terminal (Fig. 3, references 18 and 20).

- 22. Claim 6 is essentially similar to Claim 4 and is rejected for the reasons stated above apropos of Claim 4.
- 23. All elements of Claim 7 are comprehended by Claim 4. Claim 7 is rejected for the reasons stated above apropos to Claim 4. (Fig. 3; column 2, lines 30-45).
- 24. Regarding Claim 11, Korner discloses a capacitor type of microphone having a microphone output terminal, the microphone comprising: a movable electrode (22); a fixed electrode (24) arranged face to face to the movable electrode; first amplification circuit (20); and second amplification circuit (18) cascaded to the first amplification circuit between an output terminal of the first amplification circuit and a microphone output terminal and formed to have an FET (field effect transistor) of which gate is grounded to form a gate-common transistor circuit (i.e. the gate is maintained at ac common, therefore the gate is grounded to form a gate-common transistor circuit)(Fig. 3; column 2, lines 30-46; column 3, lines 6-15). Korner does not expressly disclose a member for shielding the microphone from electromagnetic waves. However it would have been obvious to one having ordinary skill in the art to provide a member for shielding the microphone comprising the movable electrode, the fixed electrode, the first amplification circuit, and the second amplification circuit in order to avoid external electromagnetic interference as taught by Cote. Cote disclose a capacitor microphones

Art Unit: 2644

in which a permanently polarized dielectric, or electret, is used requires a local preamplifier circuit because of their low output which must be well shielded to avoid external electromagnetic interference (column 1, lines 6-16; column 1, line 67 to column 2, line 5; column 2, lines 43-68). Therefore it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify Korner with the teaching of Cote to shield the microphone comprising the movable electrode, the fixed electrode, the first amplification circuit, and the second amplification circuit in order to avoid external electromagnetic interference.

Conclusion

25. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Art Unit: 2644

26. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Corey P Chau whose telephone number is (703)305-0683. The examiner can normally be reached on Monday - Friday 9:00 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Forester W Isen can be reached on (703)305-4386. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

November 29, 2004

YU MEI PRIMARY EXAMINER